

IGBT

Features

- 1200V,15A
- V_{CE(sat)(typ.)}=1.7V@V_{GE}=15V,I_C=15A
- High speed switching
- Higher system efficiency
- Soft current turn-off waveforms
- Square RBSOA

General Description

Absolute Maximum Ratings

JIAEN FS-IGBTs offer lower losses and higher energy efficiency for application such as IH (induction heating),UPS, general inverter and other soft switching applications.

Symbol 2.Collector 1.Gate 3.Emitter TO-247 G C E

JNG15T120HFU1

Symbol	Parameter	Value	Units
VCES	Collector-Emitter Voltage	1200	V
V _{GES}	Gate-Emitter Voltage	<u>+</u> 30	V
I	Continuous Collector Current (Tc=25 $^\circ\!{\rm C}$)	30	А
lc	Continuous Collector Current (Tc=100 $^{\circ}$ C)	15	А
Ісм	Pulsed Collector Current (Note 1)	45	А
l _F	Diode Continuous Forward Current (Tc=100 $^\circ\!\!\!\mathrm{C}$)	15	А
IFM	Diode Maximum Forward Current (Note 1)	45	А
t _{sc}	Short Circuit Withstand Time	10	us
D	Maximum Power Dissipation (T _c =25 $^{\circ}$ C)	236	W
PD	Maximum Power Dissipation ($T_{C}\text{=}100^{\circ}\text{C}\text{)}$	118	W
TJ	Operating Junction Temperature Range	-55 to +175 ℃	
Tstg	Storage Temperature Range	-55 to +150	°C

Thermal Characteristics

Symbol	Parameter	Max.	Units	
Rth j-c Thermal Resistance, Junction to case for IGBT		0.64	°C/ W	
Rth j-cThermal Resistance, Junction to case for Diode1.5		°C/ W		
R _{th j-a}	Thermal Resistance, Junction to Ambient	40	°C/ W	



Electrical Characteristics ($T_c=25^{\circ}C$ unless otherwise noted)

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Units
BV _{CES}	Collector-Emitter Breakdown Voltage	V _{GE} = 0V, I _C = 250uA	1200	-	-	V
I _{CES}	Collector-Emitter Leakage Current	V _{CE} = 1200V, V _{GE} = 0V	-	-	100	uA
I _{GES}	Gate Leakage Current, Forward	$V_{GE} = + 30V, V_{CE} = 0V$	-	-	<u>+</u> 100	nA
V _{GE(th)}	Gate Threshold Voltage	$V_{GE} = V_{CE}, I_C = 250 \text{uA}$	4.5	-	6.5	V
V _{CE(sat)}	Collector-Emitter Saturation Voltage	V _{GE} =15V, I _C = 15A	-	1.7		V
Qg	Total Gate Charge	V _{cc} =960V	-	81		nC
Qge	Gate-Emitter Charge	V _{GE} =15V	-	24.3		nC
Qgc	Gate-Collector Charge	IC=15A	-	44.8		nC
t d(on)	Turn-on Delay Time	Vcc=600V	-	25	-	ns
t r	Turn-on Rise Time		-	29	-	ns
t d(off)	Turn-off Delay Time	V _{GE} =15V	-	150	-	ns
t f	Turn-off Fall Time	I _C =15A R _G =15Ω Inductive Load T _C =25 ℃	-	98	-	ns
Eon	Turn-on Switching Loss		-	0.7	-	mJ
Eoff	Turn-off Switching Loss		-	0.5	-	mJ
Ets	Total Switching Loss		-	1.2	-	mJ
Cies	Input Capacitance	– V _{CE} =25V V _{GE} =0V f = 1MHz	-	1340	-	pF
Coes	Output Capacitance		-	71	-	pF
Cres	Reverse Transfer Capacitance		-	17	-	pF

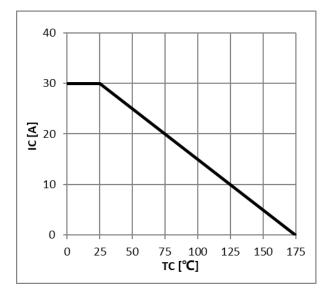
Electrical Characteristics of Diode (Tc=25°C unless otherwise noted)

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Units
V _F	Diode Forward Voltage	I _F =15A	-	2.2	2.6	V
trr	Diode Reverse Recovery Time	V _{CE} = 600V	-	252		ns
l r r	Diode peak Reverse Recovery Current	I _F = 15A	-	16		A
Qr r	Diode Reverse Recovery Charge	dIF/dt = 800A/us	-	1137		nC

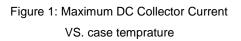
Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature





Typical Performance Characteristics



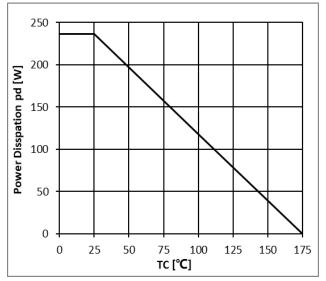


Figure 2: Power Dissipation VS. Case Temperature

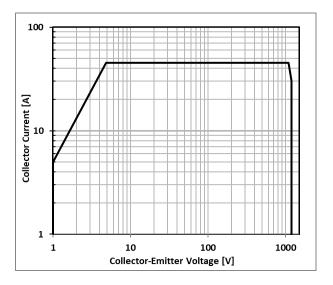


Figure 3: Reverse Bias SOA,TJ=125 $^\circ\!\!\mathrm{C}$,VGE=15V

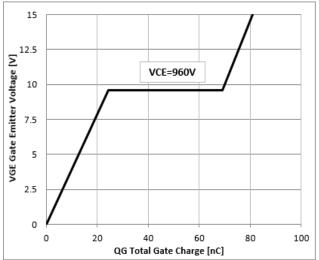


Figure 4: Typical Gate charge VS. VGE,IC=15A



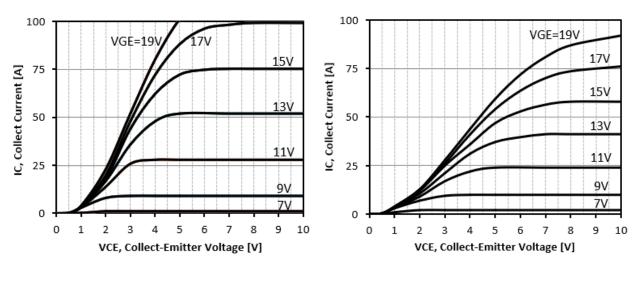
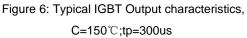


Figure 5: Typical IGBT Output characteristics, $\label{eq:TC} TC{=}25\,^\circ\!\!\mathrm{C}{;}tp{=}300us$



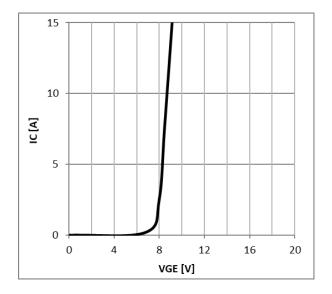


Figure 7: Typical Gate Threshold Voltage

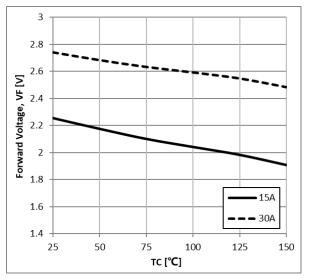


Figure 8: Typical Forward Voltage vs IF



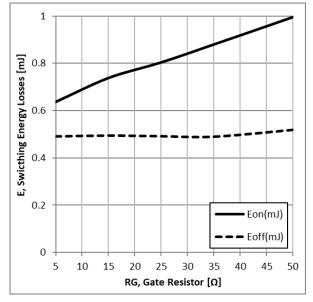


Figure 9: Typical Energy Loss VS. RG, TC=25 $^\circ\!\mathrm{C}$, L=200uH,VCE=600V,VGE=15V,IC=15A

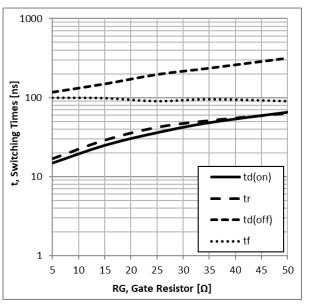


Figure 10: Typical Switching Time VS. RG, TC=25 $^\circ\!\mathrm{C}$, L=200uH,VCE=600V,VGE=15V,IC=15A

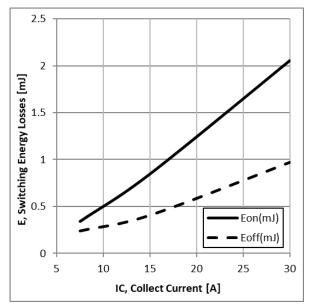


Figure 11: Typical Energy Loss VS. IC,TC=25 $^{\circ}$ C, L=200uH,VCE=600V, VGE=15V,RG=15 $^{\Omega}$

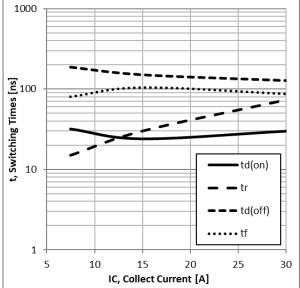


Figure 12: Typical Switching Time VS. IC,TC=25 $^\circ\!\mathrm{C}$, L=200uH,VCE=600V,VGE=15V,RG=15 Ω



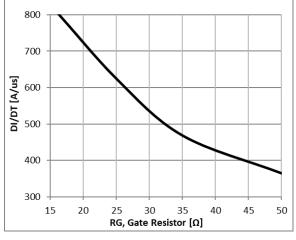
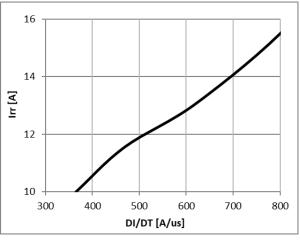
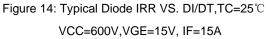
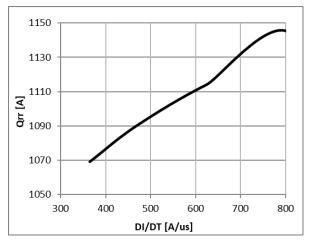
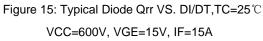


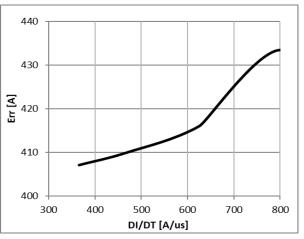
Figure 13: Typical Diode DI/DT VS. RG,TC=25 $^\circ\!\!\mathbb{C}$ VCC=600V, VGE=15V, IF=15A

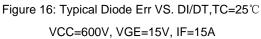














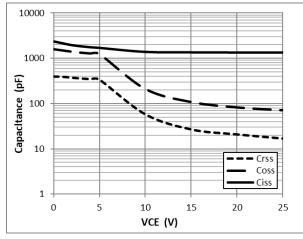


Figure 17: Typical Capacitance VS. VCE, VGE=0V,f=1MHz

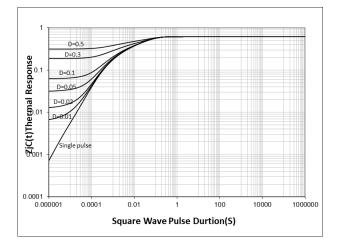
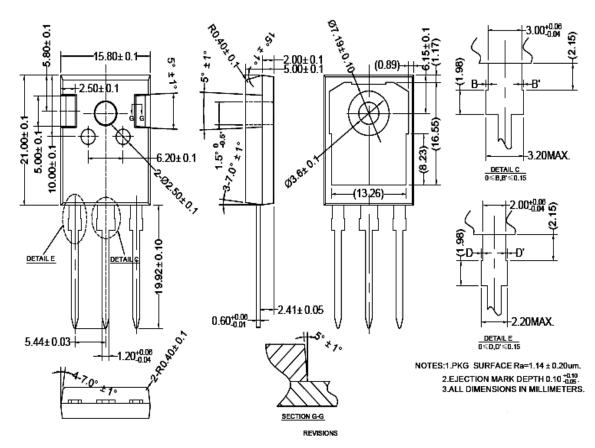


Figure 18: Normalized transient thermal impedance junction-to-case



TO247 PACKAGE OUTLINE



公差值	表面粗糙度		
±0.2	Ra3.2~6.3		
±0.1	Ra1.6~3.2		
±0.01	Ra0.8~1.6		
±0.005	Ra0.4~0.8		
±0.002	Ra0.2~0.4		
	±0.2 ±0.1 ±0.01 ±0.005		

0≤D,D'≤0.15

NOTES:1.PKG_SURFACE Ra=1.14 ± 0.20um. 2.E JECTION MARK DEPTH 0.10 +0.05 3.ALL DIMENSIONS IN MILLIMETERS.



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