

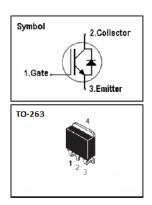
IGBT

Features

- 650V,20A
- V_{CE(sat)(typ.)}=1.85V@V_{GE}=15V,I_C=20A
- High speed switching
- Higher system efficiency
- Soft current turn-off waveforms
- Square RBSOA using NPT technology



JIAEN NPT IGBTs offer lower losses and higher energy efficiency for application such as IH (induction heating),UPS, general inverter and other soft switching applications.



Absolute Maximum Ratings

Symbol	Parameter	Value	Units
Vces	Collector-Emitter Voltage	650	V
V _{GES}	Gate-Emitter Voltage	<u>+</u> 30	V
1-	Continuous Collector Current (Tc=25 ℃)	40	А
lc	Continuous Collector Current (Tc=100°C)	20	А
Ісм	Pulsed Collector Current (Note 1)	60	А
l _F	I _F Diode Continuous Forward Current (T _C =100 °C) 20		А
I _{FM}	Diode Maximum Forward Current (Note 1)	60	А
t _{sc}	t _{sc} Short Circuit Withstand Time		us
D	Maximum Power Dissipation (Tc=25 °C)	140	W
P _D	Maximum Power Dissipation (Tc=100°C)	56	W
TJ	Operating Junction Temperature Range	-55 ~150	$^{\circ}$ C
T _{STG}	Storage Temperature Range	-55 ~150	$^{\circ}$

Thermal Characteristics

Symbol	Parameter	Max.	Units
R _{th j-c}	Thermal Resistance, Junction to case for IGBT	0.9	°C/W
R _{th j-c}	Thermal Resistance, Junction to case for Diode	1.6	°C/W
R _{th j-a}	Thermal Resistance, Junction to Ambient	62.5	°C/W



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$\underline{\textbf{Electrical Characteristics}} \text{ (Tc=25\,^{\circ}C unless otherwise noted)}$

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Units
BV _{CES}	Collector-Emitter Breakdown Voltage	$V_{GE} = 0V, I_{C} = 250uA$	650	-	-	V
I _{CES}	Collector-Emitter Leakage Current	$V_{CE} = 650 V, V_{GE} = 0 V$	-	-	100	uA
I _{GES}	Gate Leakage Current	V_{GE} = $\pm 30V$, V_{CE} = $0V$	-	-	±100	nA
$V_{GE(th)}$	Gate Threshold Voltage	$V_{GE} = V_{CE}$, $I_{C} = 250uA$	4.5	-	6.5	V
V _{CE(sat)}	Collector-Emitter Saturation Voltage	V_{GE} =15V, I_{C} = 20A	-	1.85	2.5	V
Qg	Total Gate Charge	Vcc=480V	-	39.7		nC
Qge	Gate-Emitter Charge	V _{GE} =15V	-	7.0		nC
Qgc	Gate-Collector Charge	Ic=20A	-	21.9		nC
t d(on)	Turn-on Delay Time		-	18	-	ns
t r	Turn-on Rise Time	V _{CC} =400V V _{GE} =15V	-	26	-	ns
t d(off)	Turn-off Delay Time		-	98	-	ns
t f	Turn-off Fall Time	Ic=20A R _G =15Ω	-	89	-	ns
Eon	Turn-on Switching Loss	Inductive Load	-	0.42	-	mJ
Eoff	Turn-off Switching Loss	Tc=25 ℃	-	0.88	-	mJ
Ets	Total Switching Loss		-	1.3	-	mJ
C _{ies}	Input Capacitance	V _{CE} =25V	-	717	-	pF
Coes	Output Capacitance	V _{GE} =0V	-	39.5	-	рF
C _{res}	Reverse Transfer Capacitance	f = 1MHz	-	23.2	-	pF
RGint	Integrated gate resistor			2.14		Ω

Electrical Characteristics of Diode (Tc=25°C unless otherwise noted)

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Units
V_{F}	Diode Forward Voltage	I _F =20A	1	1.9	2.5	V
trr	Diode Reverse Recovery Time	V _{CE} = 400V	-	60		ns
Irr	Diode peak Reverse Recovery Current	I _F = 20A	-	13.75		Α
Qrr	Diode Reverse Recovery Charge	Rg=15 Ω	-	459		nC

Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature



Typical Performance Characteristics

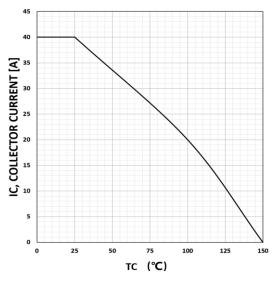


Figure 1. Maximum DC collector current VS. case temperature

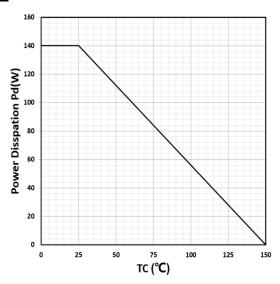


Figure 2. Power dissipation VS. case temperature

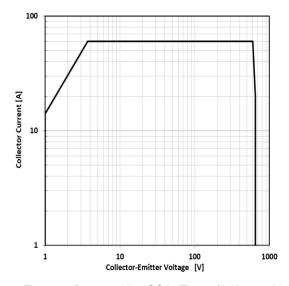


Figure 3. Reverse bias SOA, Tj=125 $^{\circ}$ C,Vge=15V

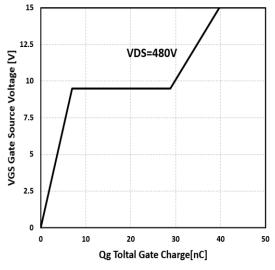


Figure4:Typical gate charge VS. VGE,IC=20A





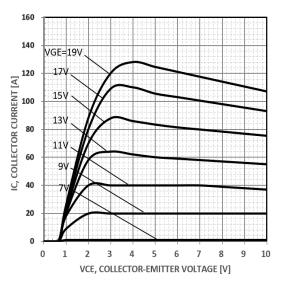


Figure 5. Typical output characteristics tp=300us $\,$ Tc=25 $\,$ °C

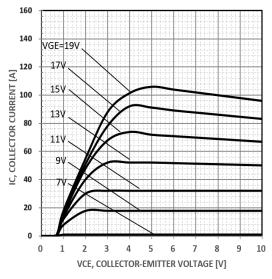


Figure 6. Typical output characteristics tp=300us Tc=150 $^{\circ}$ C

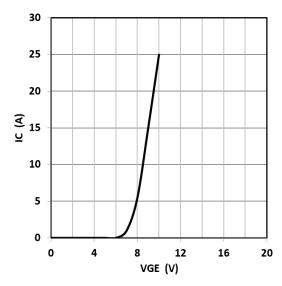


Figure 7. Typical gate threshold voltage

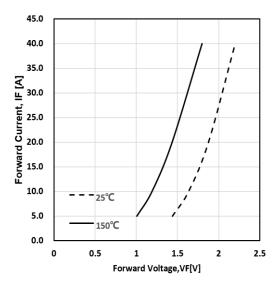


Figure 8. Typical forward voltage vs IF



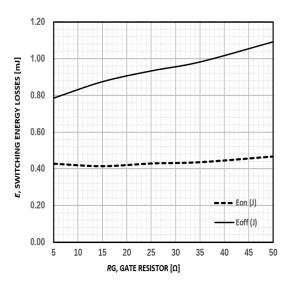


Figure9: Typical energy loss VS. Rg,TC=25°C, VCE=400V, VGE=15V ,IC=20A

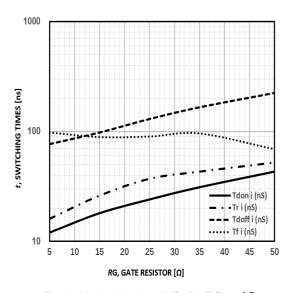


Figure 10: Typical switching time VS. Rg,TC=25°C, VCE=400V, VGE=15V ,IC=20A

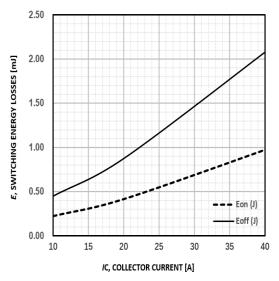


Figure11: Typical energy loss VS. IC, TC=25 $^{\circ}$ C , VCE=400V, VGE=15V ,RG=15 Ω

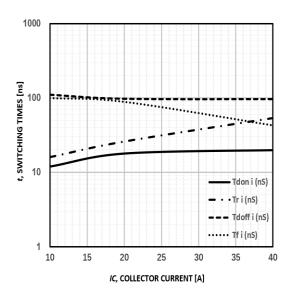


Figure 12: Typical switching time VS. IC, TC=25 $^{\circ}\text{C}$, VCE=400V, VGE=15V,RG=15 Ω





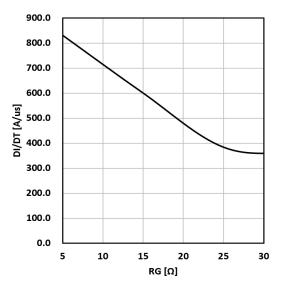


Figure 13. Typical diode di/dt vs rg $Tc=25^{\circ}C$ VCE=400V VGE=15V IF=20A

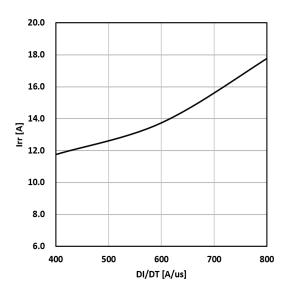


Figure 14. Typical diode irr vs di/dt Tc=25℃ VCE=400V VGE=15V IF=20A

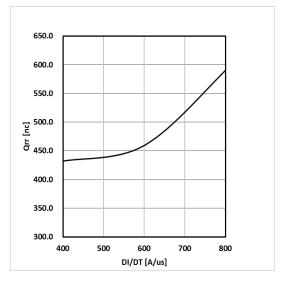


Figure 15. Typical diode Qrr vs di/dt Tc=25°C VCE=400V VGE=15V IF=20A

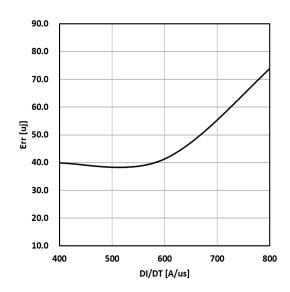


Figure 16. Typical diode Err vs di/dt Tc=25°C VCC=400V VGE=15V IF=20A





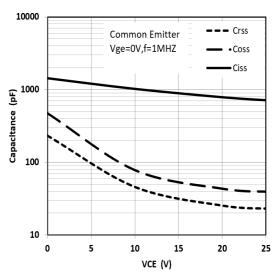


Figure17:Typical capacitance VS. VCE, VGE=0V,f=1MHz

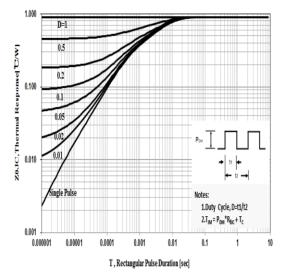
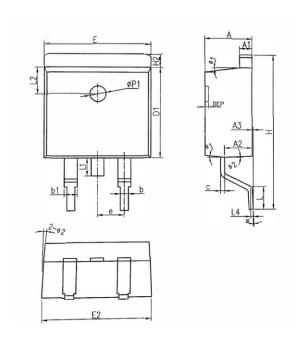


Figure 18. normalized transient thermal impedance, junction-to-case



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TO-263 PACKAGE OUTLINE



SYMBOL	MM			INCH.			
	MIN	NOM	MAX	MIN	NOM	MAX	
Α	4.40	4.57	4.70	0.173	0.180	0.185	
A1	1.22	1.27	1.32	0.048	0.050	0.052	
A2	2.59	2.69	2.79	0.102	0.106	0.110	
A3	0.00	0.10	0.20	0.000	0.004	0.008	
b	0.77	0.813	0.90	0.030	0.032	0.035	
b1	1.20	1.270	1.36	0.047	0.050	0.054	
С	0.34	0.381	0.47	0.013	0.015	0.019	
D1	8.60	8.70	8.80	0.339	0.343	0.346	
E	10.00	10.16	10.26	0.394	0.400	0.404	
E2	10.00	10.10	10.20	0.394	0.398	0.402	
е	2.54 BSC			0.100 BSC			
Н	14.70	15.10	15.50	0.579	0.594	0.610	
H2	1.17	1.27	1.40	0.046	0.050	0.055	
L	2.00	2.30	2.60	0.079	0.091	0.102	
L1	1.45	1.55	1.70	0.057	0.061	0.067	
L2		2.50	REF	0.098 REF			
L4	0.25 BSC			0.010 BSC			
9	0°	5°	8°	0°	5°	8°	
θ1	5°	7°	9°	5°	7°	9°	
θ2	1°	3°	5°	1°	3°	5°	
ФР1	1.40	1.50	1.60	0.055	0.059	0.063	
DEP	0.05	0.10	0.20	0.002	0.004	0.008	



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